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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/658,934	09/09/2003	Chia-Shun Hsiao	M-12200-1D US	3104
75	90 11/29/2004		EXAM	INER
MacPherson K	Wok Chen & Heid LLI	MAI, ANH D		
Suite 226				D . DOD 1410 (DOD
1762 Technology Drive			ART UNIT	PAPER NUMBER
San Jose, CA 95110			2814	

DATE MAILED: 11/29/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
Office Action Summers	10/658,934	HSIAO, CHIA-SHUN				
Office Action Summary	Examiner	Art Unit				
	Anh D. Mai	2814				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statule, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status						
1) Responsive to communication(s) filed on 09 Se	1)⊠ Responsive to communication(s) filed on 09 September 2003.					
2a) This action is FINAL . 2b) ⊠ This	This action is FINAL . 2b)⊠ This action is non-final.					
3) Since this application is in condition for allowar	☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims						
4)⊠ Claim(s) <u>6-10</u> is/are pending in the application.						
4a) Of the above claim(s) is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>6-10</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/or election requirement.						
Application Papers						
9) The specification is objected to by the Examiner.						
10)⊠ The drawing(s) filed on 09 September 2003 is/a	are: a)⊠ accepted or b)□ objec	ted to by the Examiner.				
Applicant may not request that any objection to the						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).						
a) All b) Some * c) None of:						
 Certified copies of the priority documents have been received. Certified copies of the priority documents have been received in Application No 						
3. Copies of the certified copies of the priority documents have been received in this National Stage						
application from the International Bureau (PCT Rule 17.2(a)).						
* See the attached detailed Office action for a list of the certified copies not received.						
Attachment(s)						
1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413) Notice of Draftsperson's Patent Drawing Review (PTO-948) Paper No(s)/Mail Date						
3) Notice of Informal Patent Application (PTO-152)						
Paper No(s)/Mail Date <u>9/9/2003; 1/13/2003 ; 4/13/2004</u> . 6) Other:						

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DETAILED ACTION

Status of the Claims

1. The Preliminary Amendment filed September 9, 2003 has been entered. Claims 1-5 have been canceled. Claims 6-10 are pending.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 2. Claims 6-10 are rejected under 35 U.S.C. 102(b) as being anticipated by Yu et al (U.S Patent No. 6,376,877) of record.

With respect to claim 6, Yu teaches an integrated circuit as claimed including:

a semiconductor substrate (102) and a nonvolatile memory cell (300) having an active area formed in the semiconductor substrate (102), the memory cell includes:

a dielectric (306) on the active area; and

a floating gate (308) on the dielectric (306), the floating gate (308) having a horizontal top surface projecting laterally beyond the active area. (See Figs. 7).

With respect to claim 7, at a location at which the top surface of the floating gate (308) projects beyond the active area, the floating gate (308) of Yu has a sidewall, and at least a top portion of the sidewall extends laterally outward and beyond the active area as the sidewall is traced upward.

With respect to claim 8, the integrated circuit of Yu further comprises a dielectric region (318) abutting the top portion of the sidewall.

With respect to claim 9, Yu teaches an integrated circuit as claimed including:

a semiconductor substrate (102) and a nonvolatile memory cell (300) having an active area formed in the semiconductor substrate (102), the memory cell comprises:

a dielectric (306) on the active area; and

a floating gate (308) on the dielectric (306), wherein the floating gate (308) has a sidewall, and at least a top portion of the sidewall extends laterally outward as the sidewall is traced upward. (See Figs. 7).

With respect to claim 10, the integrated circuit of Yu further comprises a dielectric region (318) physically contacting, and extending along, the top portion of the sidewall.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Anh D. Mai whose telephone number is (571) 272-1710. The examiner can normally be reached on 9:00AM-5:00PM.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (571) 272-1705. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Anh D. Mai

November 15, 2004